



5

1401

□ □ □ □ □

1014GSM



100□□□□

111

14GSM



500 x 500mm

SKU 

111

111

20□/□□□□4□□□□/□□

cm

46.5 x 27.2 x 17

A diagram of a 5-bit shift register. It consists of five rectangular cells connected in a horizontal line. Each cell has an input on the left and an output on the right. The inputs are labeled A, B, C, D, and E from left to right. The outputs are also labeled A, B, C, D, and E from left to right. The output of the first cell (A) is connected to the input of the second cell (B), and so on, forming a chain.

10-12□

45-50□

111

SGS□□□□100□□□□





